

Department of Electronics Engineering
BHARAT RATNA SARDAR VALLABHBHAI PATEL RAJKIYA ENGINEERING COLLEGE BASTI
Tentative B. Tech 5th Semester Timetable (2025-2026) Classroom - 227

w.e.f: 14/08/2025

Day/ Period	I 9:30-10:20am	II 10:20-11:10am	III 11:10-12:00pm	IV 12:00-12:50pm	S	V 2:30-3:20pm	VI 3:20-4:10 pm	VII 4:10-5:00pm
MON		BEC503 (SY)	BEC-054 (GJ)	BEC502 (DAS)				
TUE		BEC501 T1 & T2 (AKY)		BEC 502 (DAS)			BEC-054 (GJ)	BEC502 (DAS)
WED	BEC 057 (DAS)		BEC501 (AKY)				BEC553 (SY)	
THU	BEC502 T1 & T2 (DAS)		BNC 501 (PS)				BCC554 (DAS) (GJ)(SY)	BEC503 (SY)
FRI		BEC552 (DAS)		BEC503 T2 (SY)			BEC551 (AKY) (SY)	BEC503 (SY)
SAT	BEC501 (AKY)	BEC503 T1 (SY)	BEC 057 (DAS)					

S. No.	Subject Code	Subject Name	Name of Faculty
1	BEC-501	Integrated Circuits	Mr. Aditya Kumar Yadav (AKY)
2	BEC-502	Microprocessor and Microcontroller	Dr. Anupam Soni (DAS)
3	BEC-503	Digital Signal Processing	Mr. Sanjay Yadav (SY)
4	BEC-052	DE-I (VLSI Technology)	Miss. Garima Jaiswal (GJ)
5	BEC-057	DE-II (Optical Communication)	Dr. Anupam Soni (DAS)
6	BEC-551	Integrated Circuits Lab	Mr. Aditya Kumar Yadav (AKY), Mr. Sanjay Yadav (SY)
7	BEC-552	Microprocessor and Microcontroller Lab	Dr. Anupam Soni (DAS)
8	BEC-553	Digital Signal Processing Lab	Mr. Sanjay Yadav (SY)
9	BCC-554	Mini Project or Internship Assesment	Mr Sanjay Yadav(SY), Dr. Anupam Soni (DAS), Miss Garima Jaiswal (GJ)
10	BNC-501	Constitution of India	Mr. Prabhat Singh (PS)

